

Fig. 1



Fig. 2A

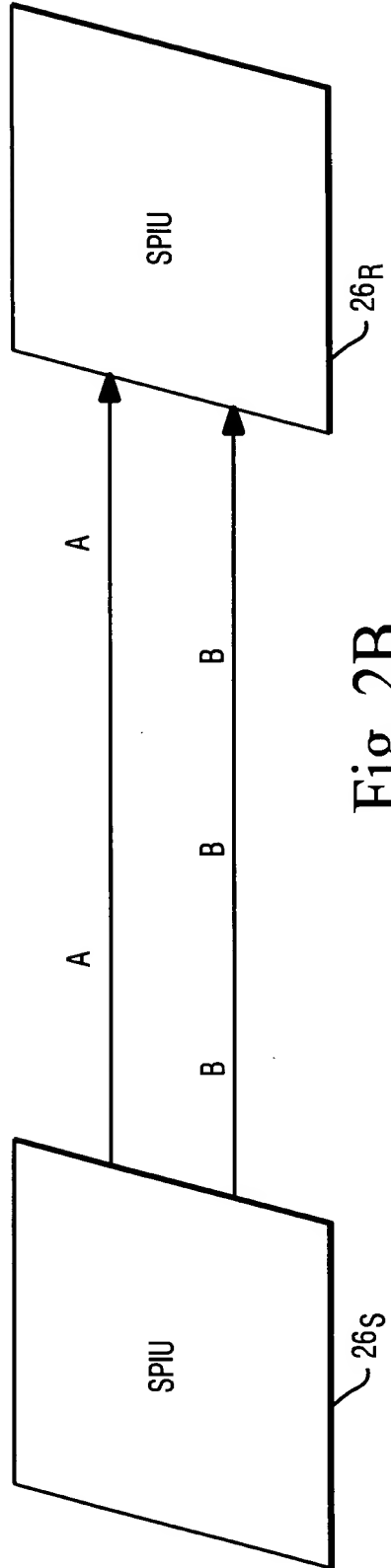


Fig. 2B



3/16

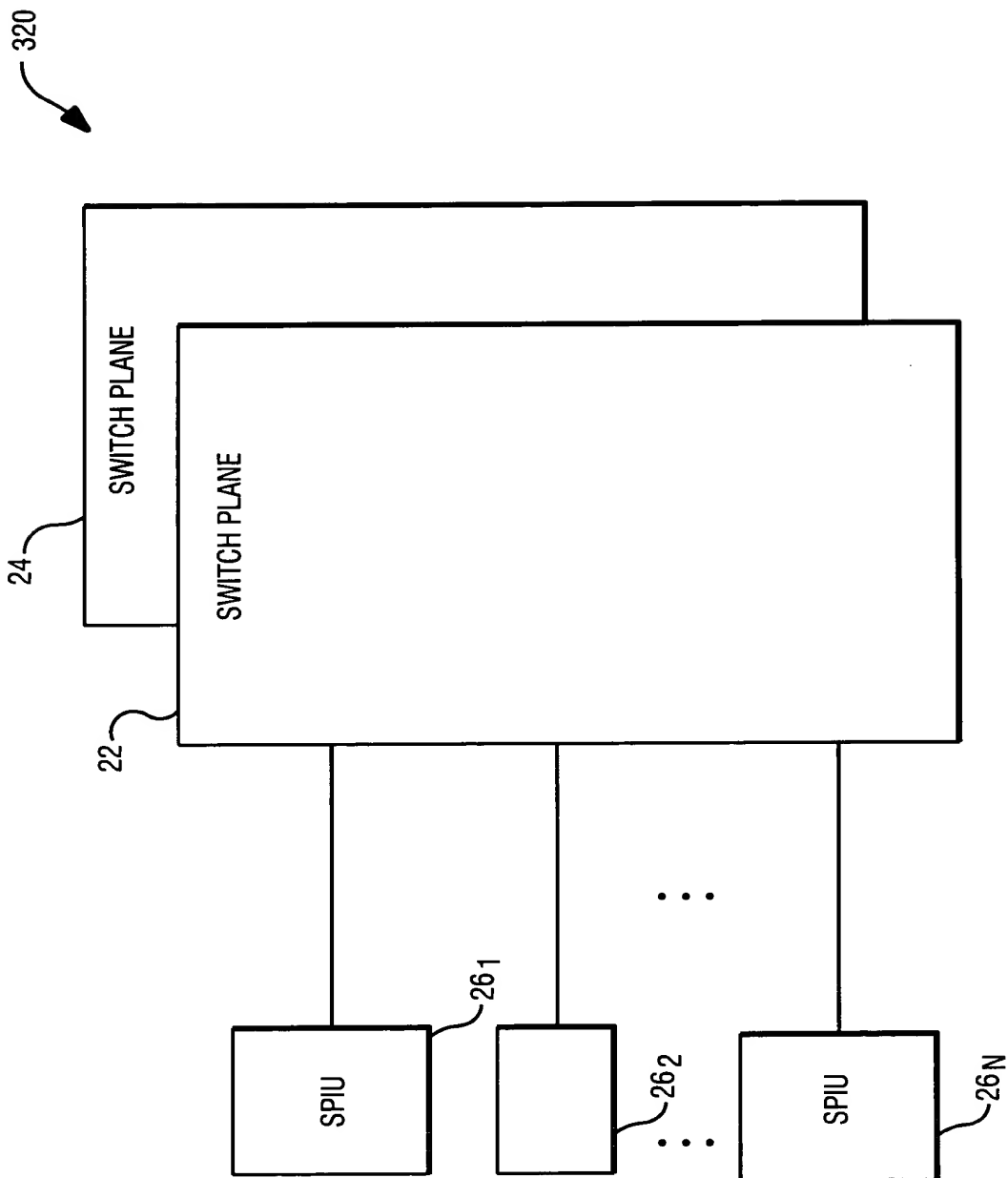


Fig. 3

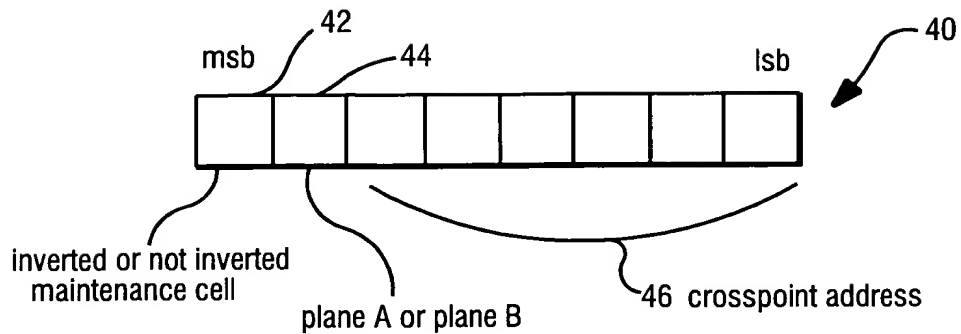


Fig. 4A

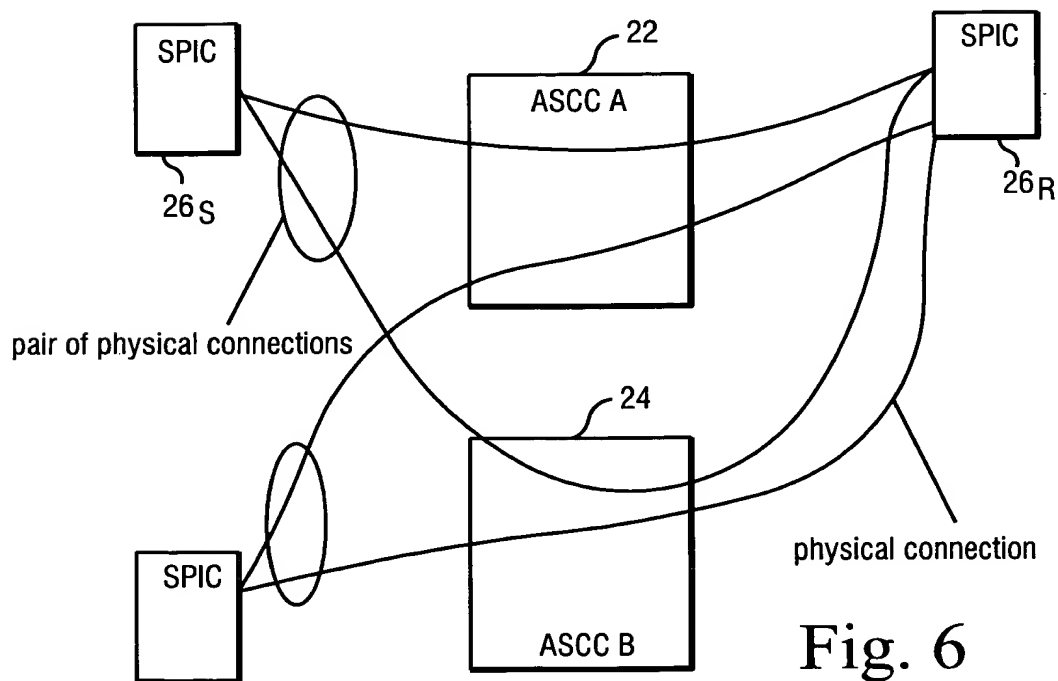


Fig. 6



5/16

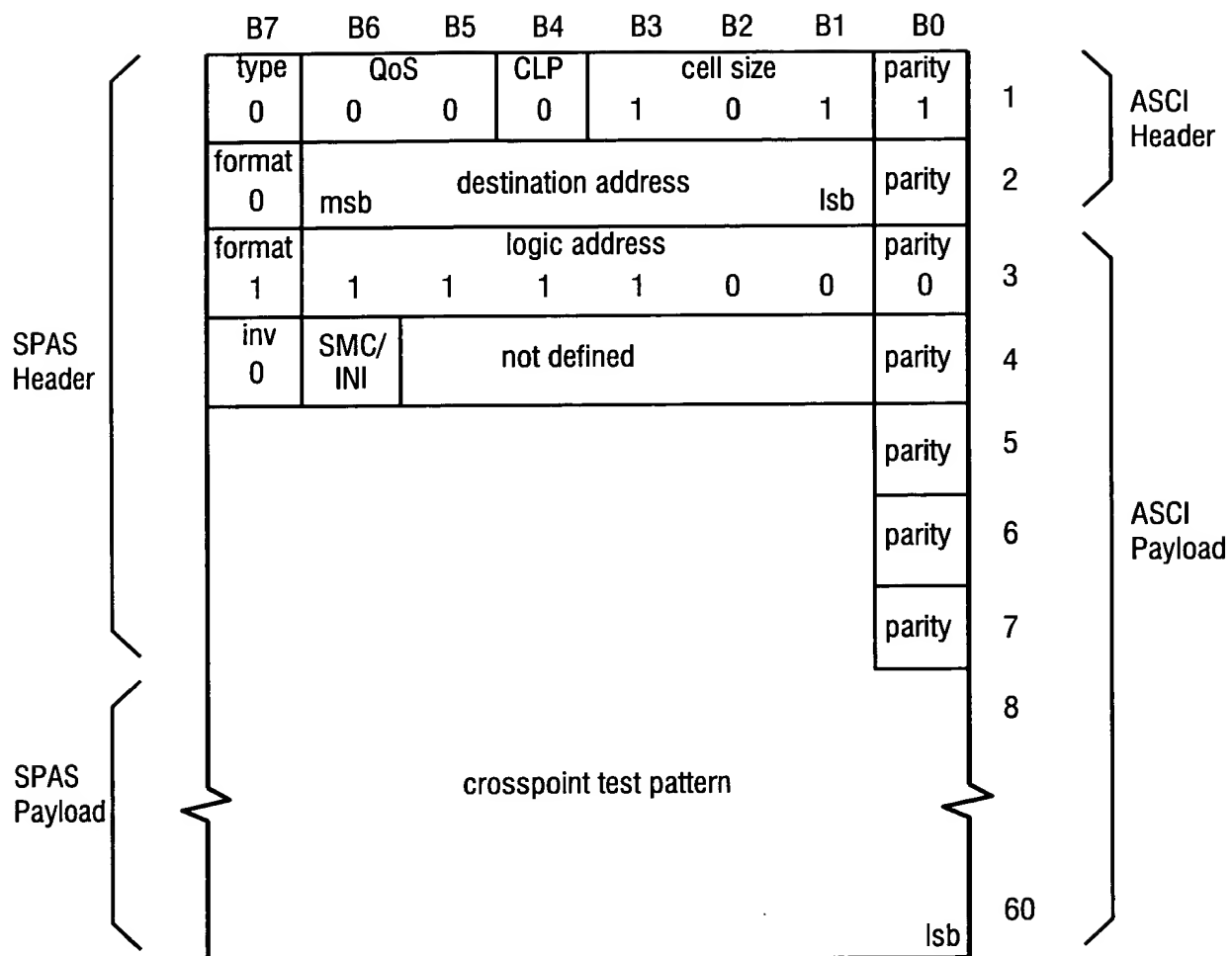


Fig. 4B(1) Initialisation Cell



6/16

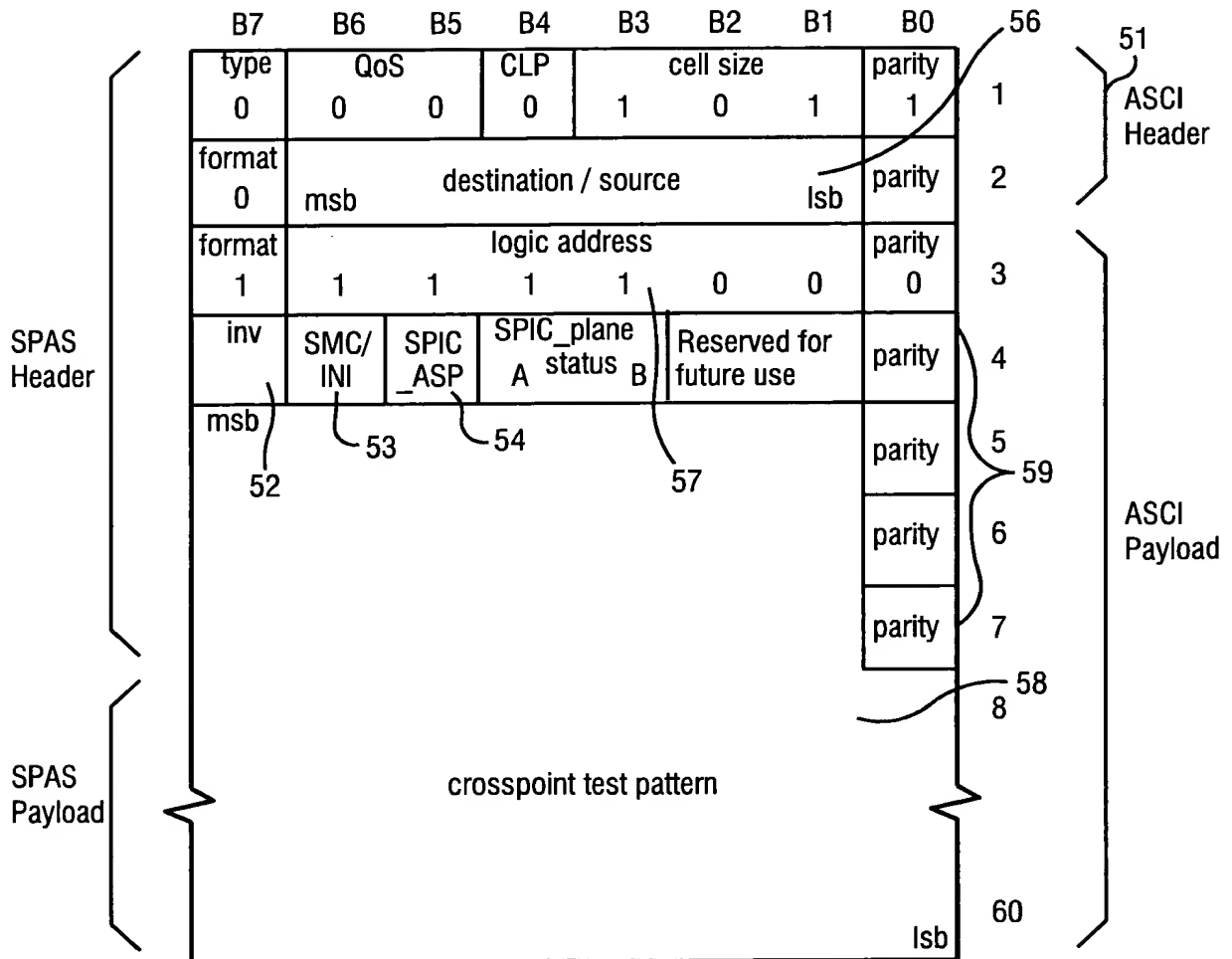


Fig. 4B(2)

Switch Maintenance Cell



Fig. 5A

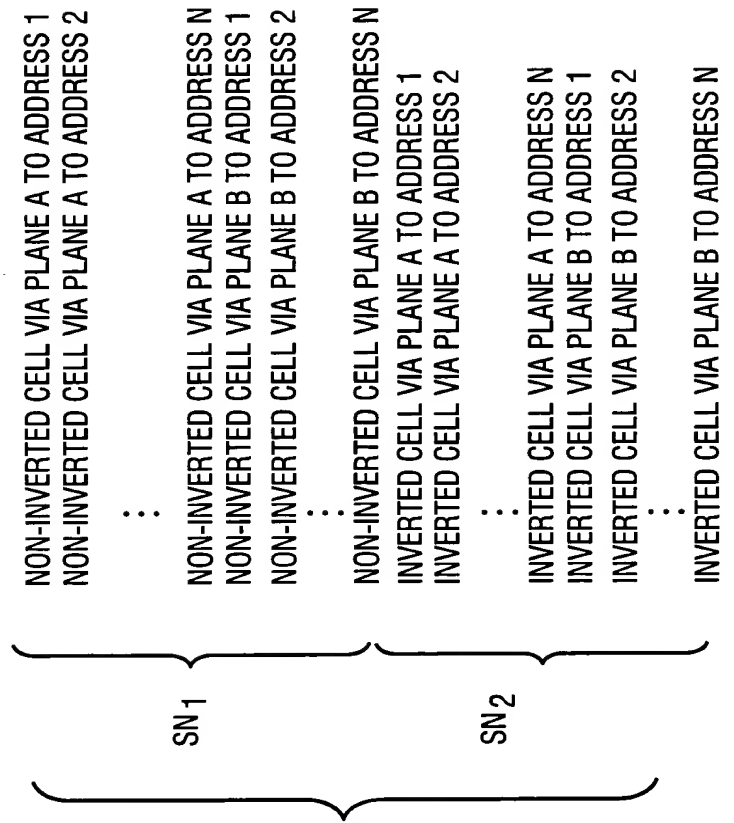


Fig. 5B

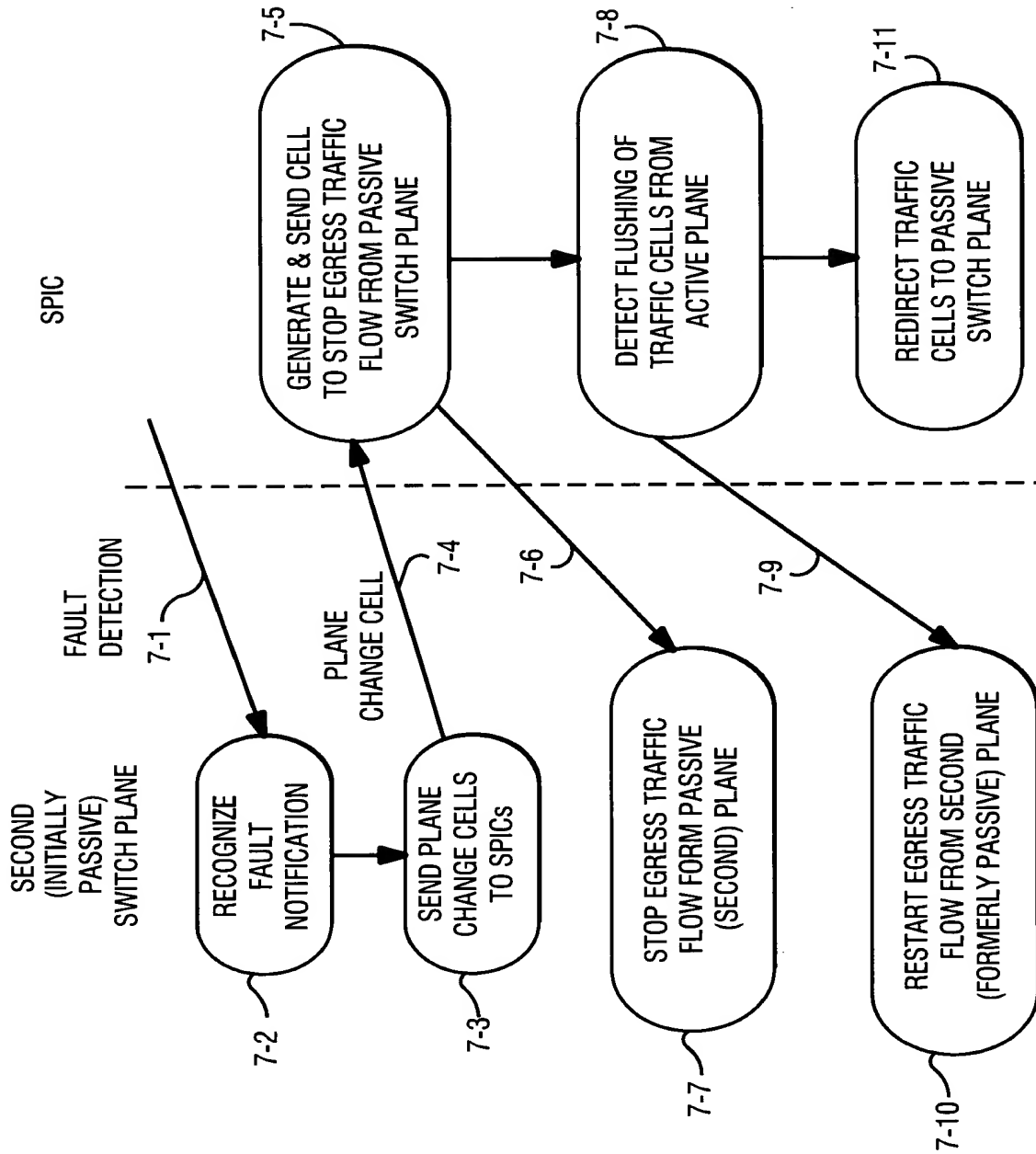


Fig. 7

9/16

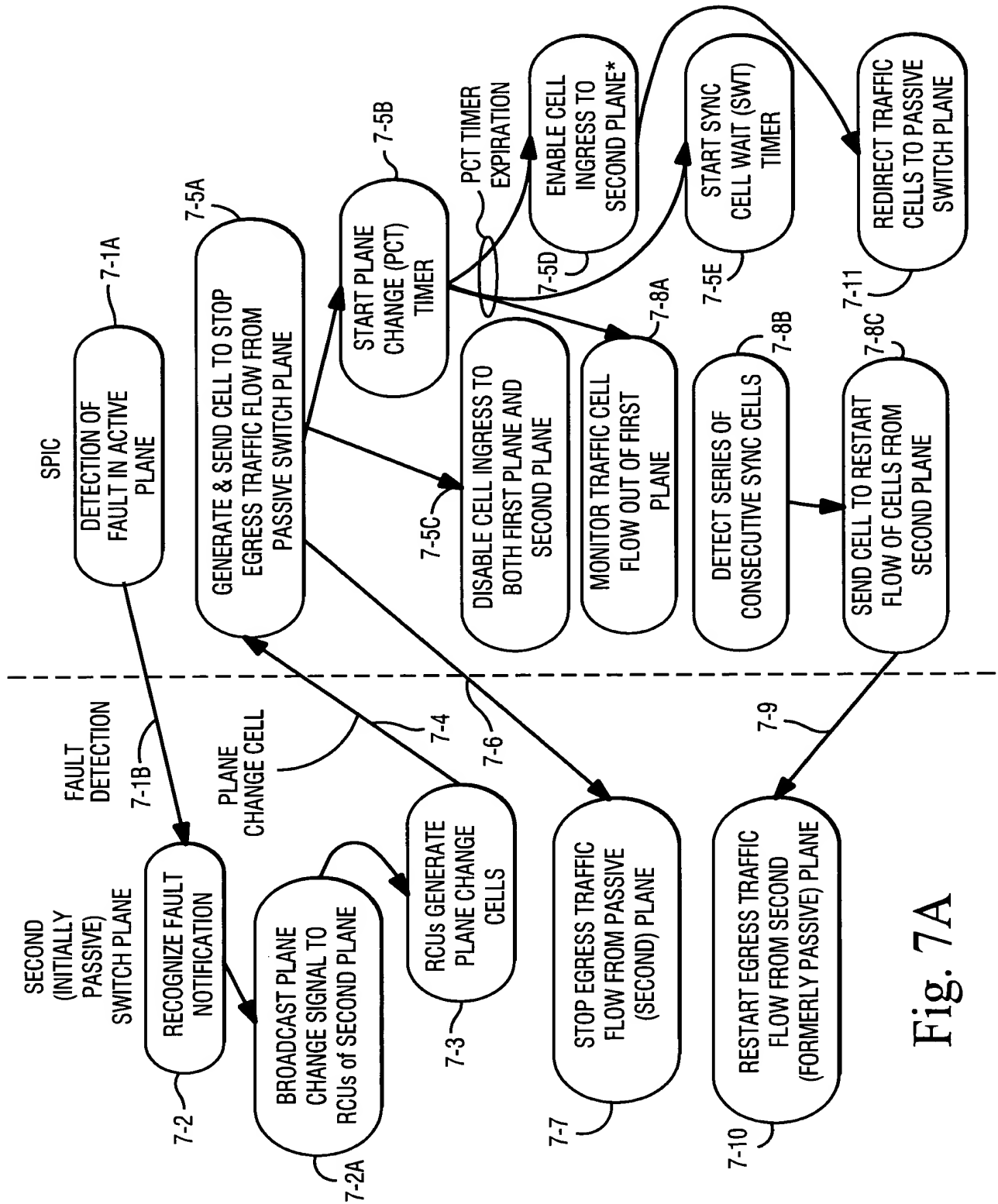


Fig. 7A



10/16

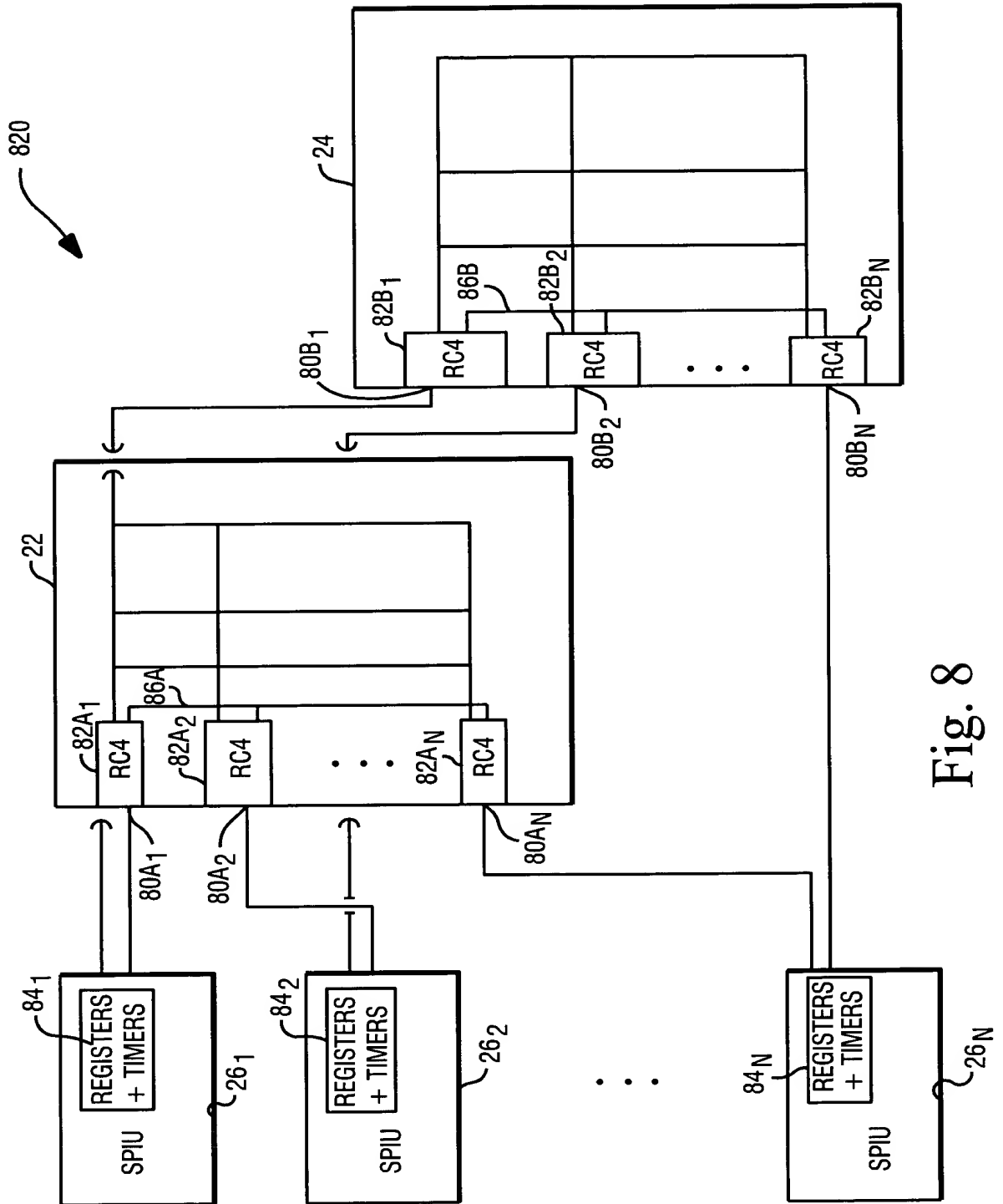


Fig. 8



11/16

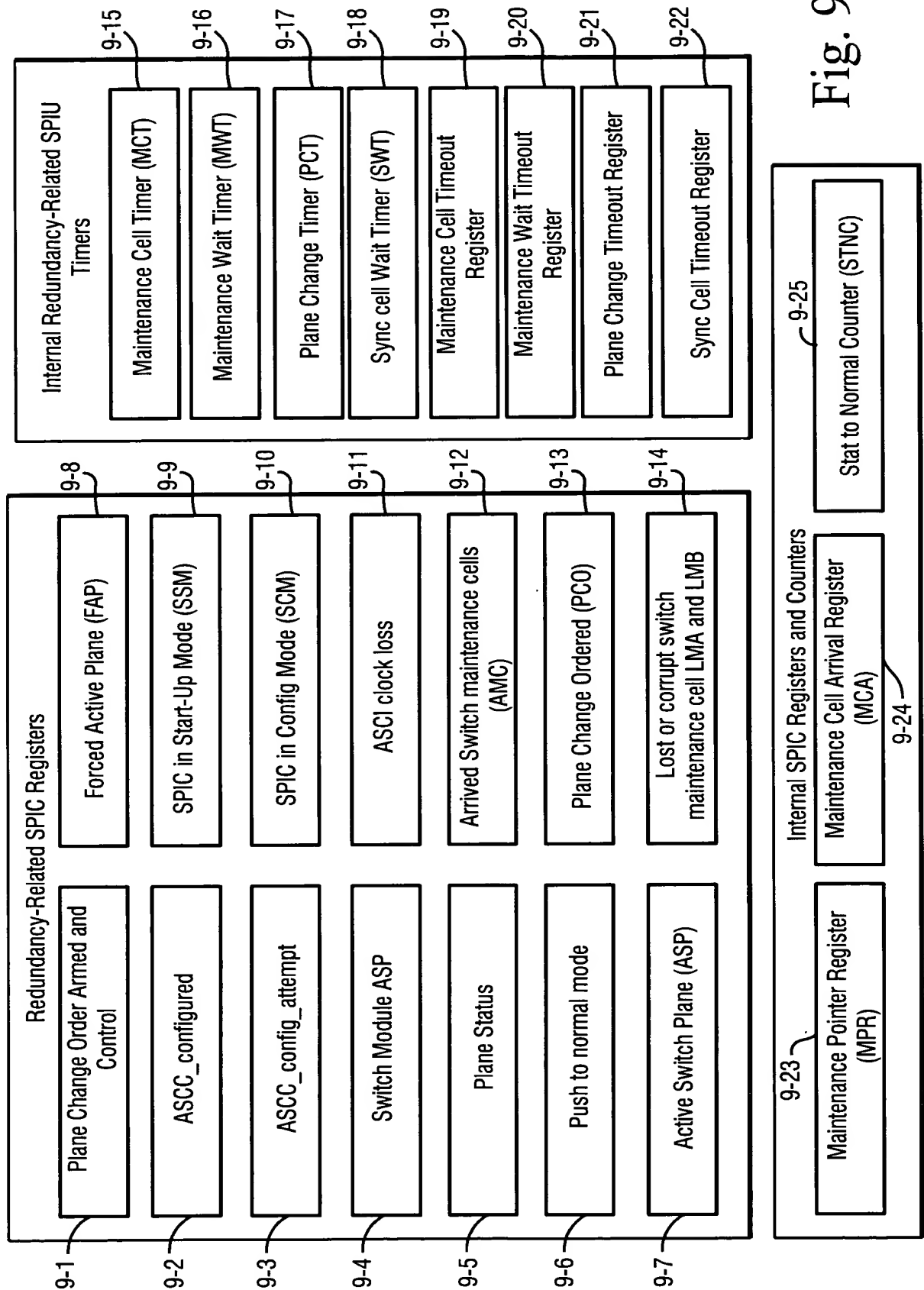


Fig. 9



12/16

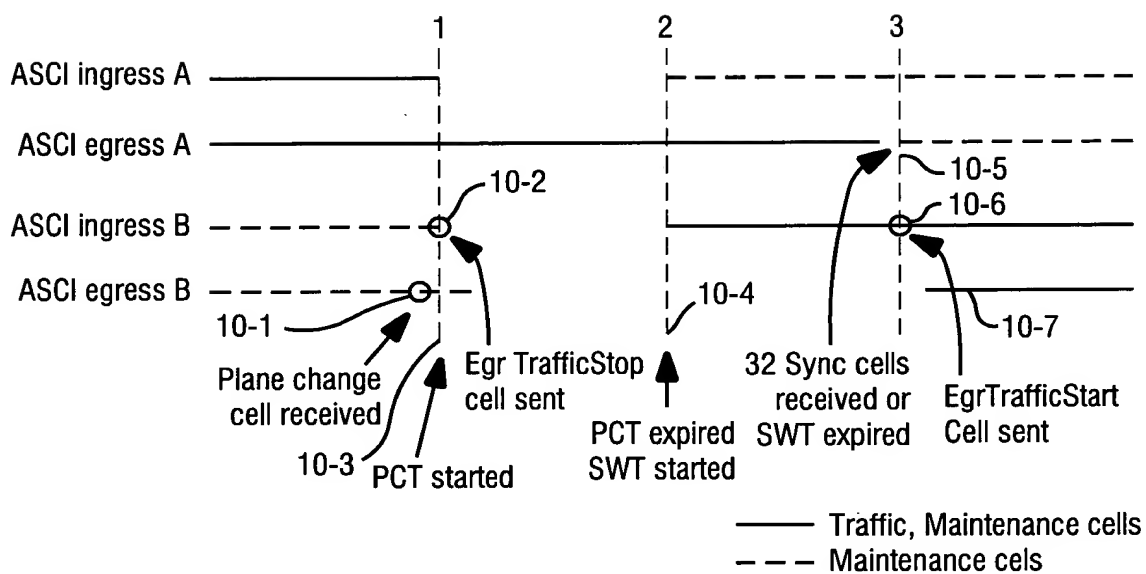


Fig. 10

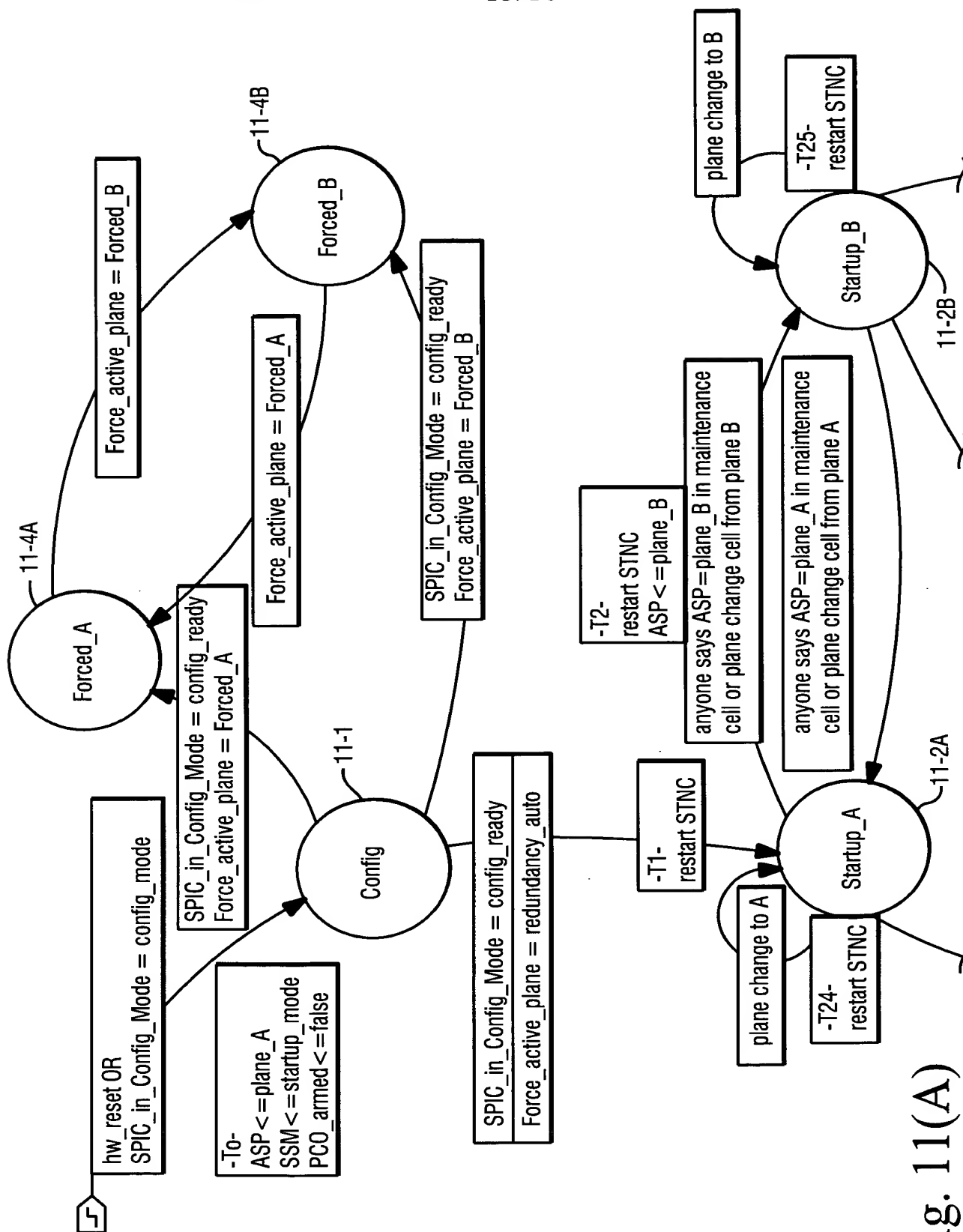
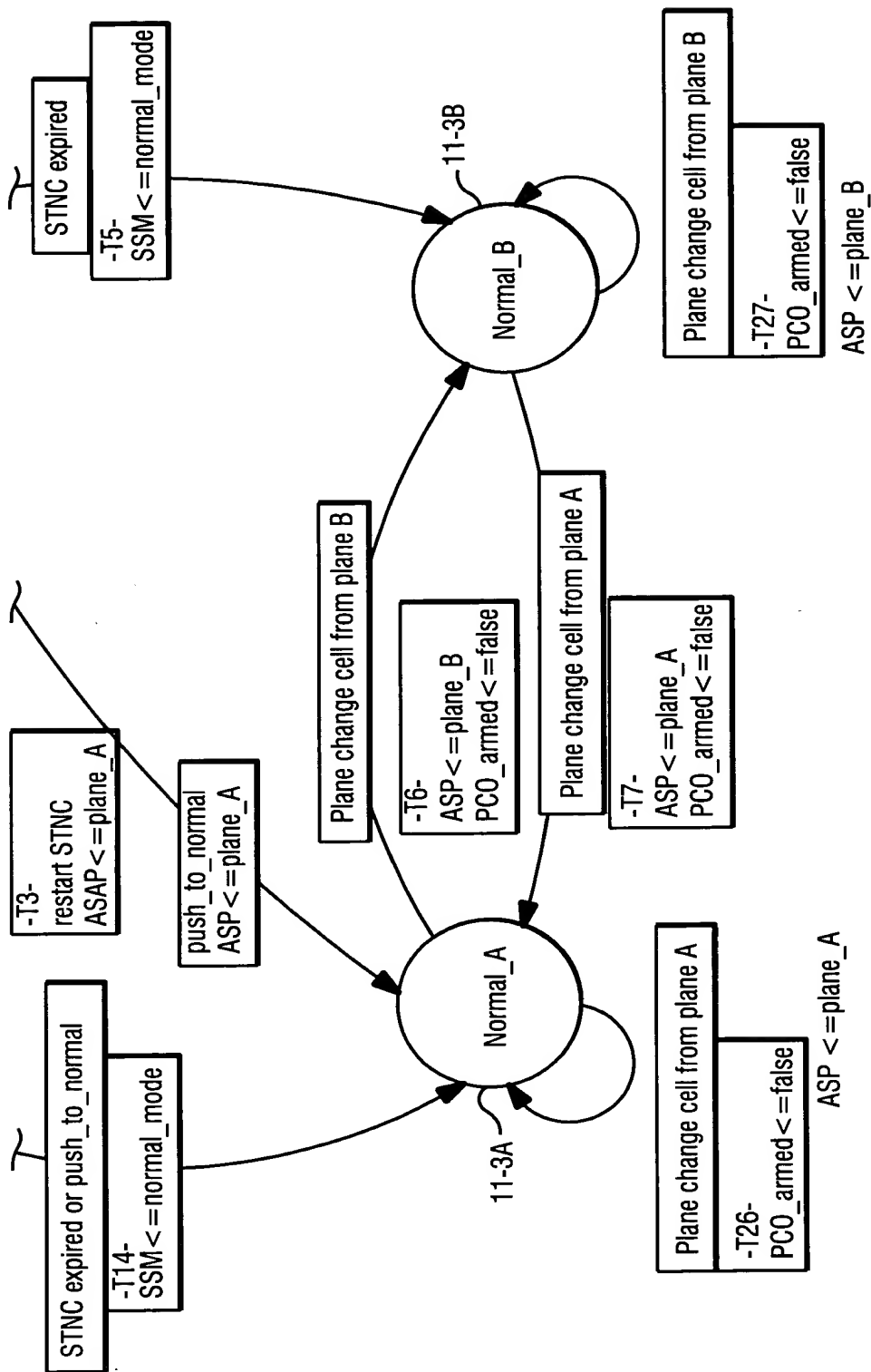


Fig. 11(A)



ASP = Active switch plane
 SSM = SPIC in start-up mode
 PCO_armed = Plain change order armed
 STNC = Start to normal counter

Fig. 11(B)

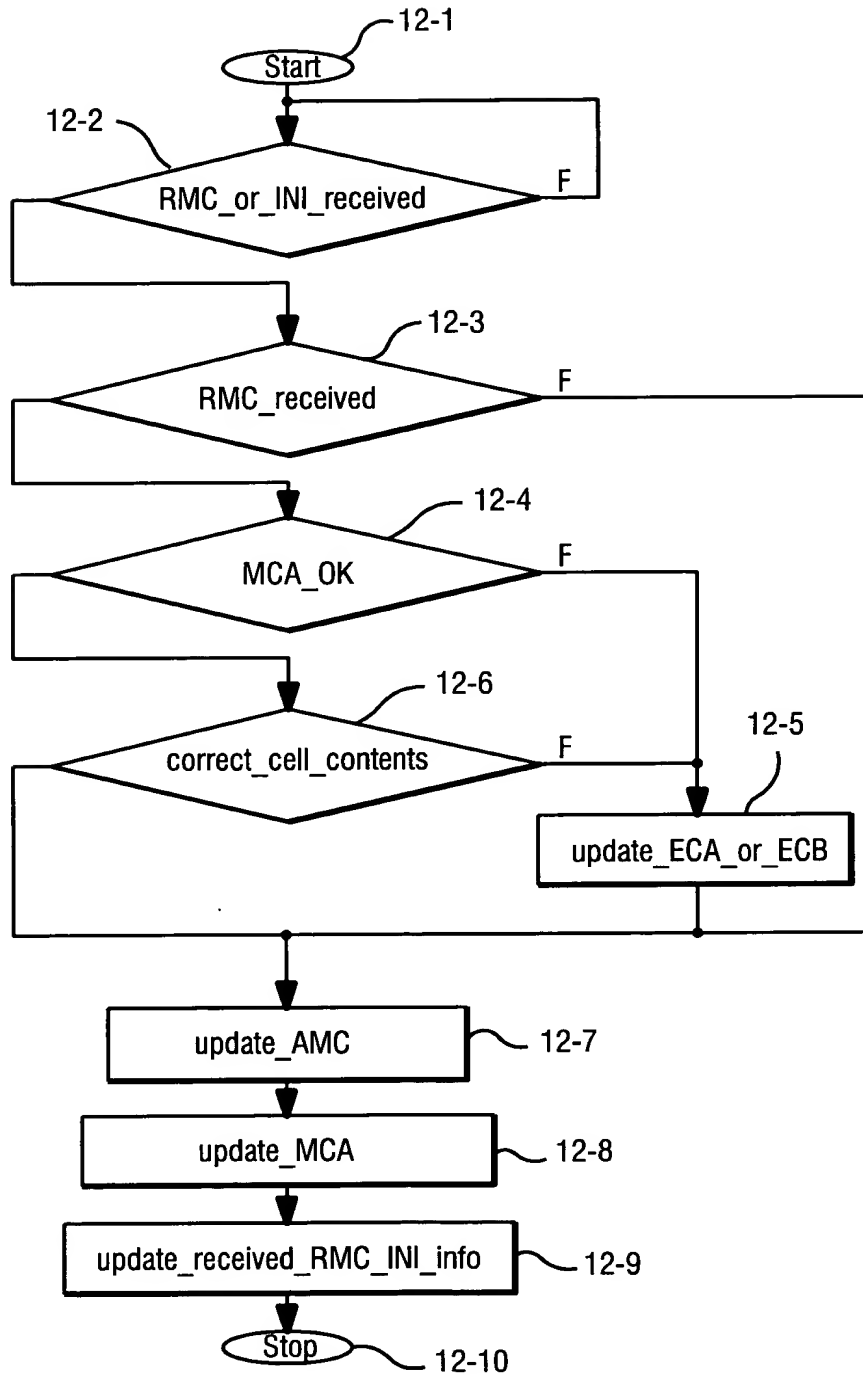


Fig. 12



16/16

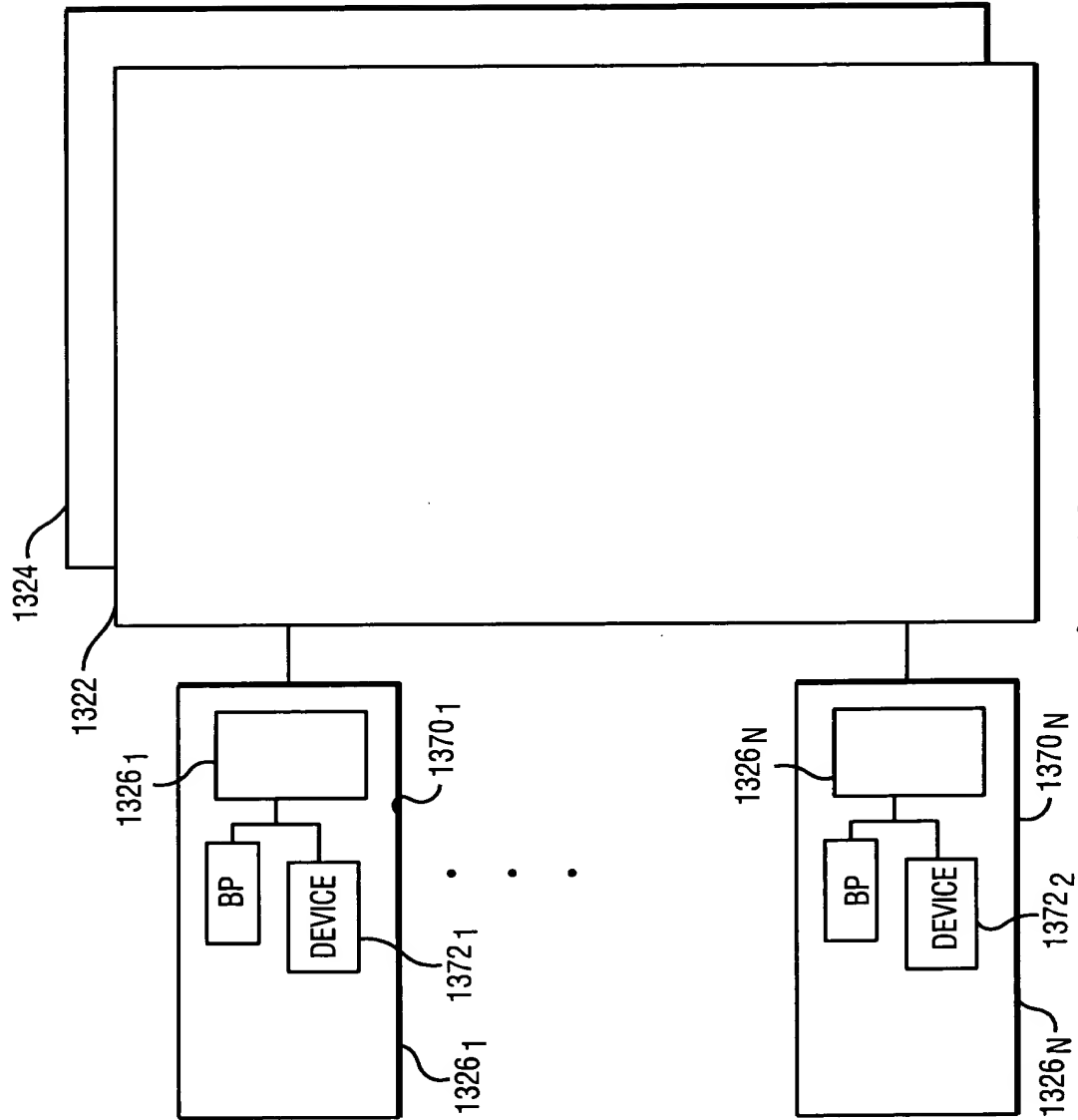


Fig. 13